> Dupline ${ }^{\circledR}$ Field- and Installationbus Transmitter for Analog Current Signals Types FFD 1530, FFD 1531, FFD 1532


## Product Description

Dupline transmitters for external supply. Standard current signal input ( 0 to 1 mA ,

0 to $20 \mathrm{~mA}, 4$ to 20 mA ). Convert analog current signals into binary codes.

- Transmitter with current input
- Current input signals: FFD 1530: $1 \times 0$ to 1 mA FFD 1531: $1 \times 0$ to 20 mA FFD 1532: 1 x 4 to 20 mA
- 8-bit (8 channels) resolution
- Binary transmission
- Enable input function
- D-housing
- Plug-in type module
- AC power supply


## Type Selection

Supply

24 VAC
120 VAC
220 VAC
Code module

Ordering no
1 signal
0 to 1 mA
FFD 1530024
FFD 1530120
FFD 1530220
FMK A to FMK P

Ordering Key
Type: Dupline
Input signal
Supply nal

FFD 1530024

$\square$

| Ordering no. <br> 1 signal <br> 0 to 20 mA | Ordering no. <br> 1 signal <br> 4 to 20 mA |
| :--- | :--- |
| FFD 1531024 FFD 1532024 <br> FFD 1531 120 FFD 1532120 <br> FFD 1531 220 <br> FMK 1532 220  |  |
| FMK A to FMK P | FMK A to FMK P |

Input Specifications

|  | FFD 1530 ... ( 8 channels) | FFD 1531 ... ( 8 channels) | FFD 1532 ... (8 channels) |
| :---: | :---: | :---: | :---: |
| Signal input | 1 current input | 1 current input | 1 current input |
| Signal range | 0 to 1 mA | 0 to 20 mA | 4 to 20 mA |
| Zero adjustment ( $\mathrm{X}_{1}$ ) | None | None | None |
| Span adjustment ( $\mathrm{X}_{2}$ ) | None | None | None |
| Span | None | None | None |
| Input resistance | $\leq 470 \Omega$ | $\leq 47 \Omega$ | $\leq 47 \Omega$ |
| Resolution | 8 bits (3.92 $\mu \mathrm{A} / \mathrm{LSB}$ ) | 8 bits (78.43 $\mu \mathrm{A} / \mathrm{LSB}$ ) | 8 bits ( $62.75 \mu \mathrm{~A} / \mathrm{LSB}$ ) |
| Settling time | $\leq 1$ pulse train +10 ms | $\leq 1$ pulse train +10 ms | $\leq 1$ pulse train +10 ms |
| Open circuit monitoring | None | None | None |
| Inaccuracy (ref. temp. $20^{\circ} \mathrm{C}$ ) of full scale | $\leq 1 \%$ | $\leq 1 \%$ | $\leq 1 \%$ |
| Cable length | $\leq 3 \mathrm{~m}$ | $\leq 3 \mathrm{~m}$ | $\leq 3 \mathrm{~m}$ |
| Dielectric voltage Input - Dupline | $\geq 200$ VAC (rms) | $\geq 200$ VAC (rms) | $\geq 200$ VAC (rms) |
| Transmission enable input | 1 contact or NPN transistor | 1 contact or NPN transistor | 1 contact or NPN transistor |
| Open loop voltage | 5 VDC | 5 VDC | 5 VDC |
| Short-circuit current | 1 mA | 1 mA | 1 mA |
| Operating time for signal "1" | $\leq 1$ pulse train +10 ms | $\leq 1$ pulse train +10 ms | $\leq 1$ pulse train +10 ms |
| Operating time for signal "0" | $\leq 1$ pulse train +10 ms | $\leq 1$ pulse train +10 ms | $\leq 1$ pulse train +10 ms |
| Contact resistance | $\leq 100 \Omega$ | $\leq 100 \Omega$ | $\leq 100 \Omega$ |
| Cable length | $\leq 3 \mathrm{~m}$ | $\leq 3 \mathrm{~m}$ | $\leq 3 \mathrm{~m}$ |
| Dielectric voltage Input- Dupline | $\geq 200$ VAC (rms) | $\geq 200$ VAC (rms) | $\geq 200$ VAC (rms) |

## Supply Specifications

| Power supply | Overvoltage cat. III (IEC 60664) |
| :---: | :---: |
| Rated operational voltage |  |
| through pins A1 \& A2 220 | 230 VAC +6\%, -15\% (IEC 60038) |
| 120 | 120 VAC $\pm 10 \%$ (IEC 60038) |
| 024 | $24 \mathrm{VAC} \pm 10 \%$ |
| Frequency | 45 to 65 Hz |
| Voltage interruption | $\leq 40 \mathrm{~ms}$ |
| Rated operational power | Typ. 2.5 VA |
| Rated operational |  |
| withstand voltage 220 | 4 kV |
| 120 | 2.5 kV |
| 024 | 800 V |
| Dielectric voltage |  |
| Supply - Dupline | $\geq 2 \mathrm{kVAC}$ (rms) |
| Supply - Signal input | $\geq 2 \mathrm{kVAC}$ (rms) |
| Supply - Enable input | $\geq 2 \mathrm{kVAC}$ (rms) |

## Mode of Operation

Transmitters with current signal input. The current signal is converted into a binary value represented as the binary status of an entire channel group ( 8 bit). This binary value may be reconverted into current or voltage signals through receivers with analogue outputs (type FAD 15..) or displayed in a scaled 7segment display via D 6369 6475.

A signal change of $0.392 \%$ of full scale influences the least significant bit, which is the highest channel of the selected group ( F 8 if $\mathrm{FMK} F$ is plugged in). A signal change of $49.8 \%$ of full scale influences the most significant bit

General Specifications

| Power ON delay | Undefined, $\leq 1 \mathrm{~s}$ |
| :--- | :--- |
| Environment | IP 20 B |
| Degree of protection | $3(\mathrm{IEC} 60664)$ |
| Pollution degree | $-20^{\circ}$ to $+50^{\circ} \mathrm{C}\left(-4^{\circ}\right.$ to $\left.+122^{\circ} \mathrm{F}\right)$ |
| Operating temperature | $-50^{\circ}$ to $+85^{\circ} \mathrm{C}\left(-58^{\circ}\right.$ to $\left.+185^{\circ} \mathrm{F}\right)$ |
| Storage temperature | 20 to $80 \%$ |
| Humidity (non-condensing) |  |
| Mechanical resistance | $15 \mathrm{G} \mathrm{(11} \mathrm{ms)}$ |
| Shock $2 \mathrm{G}(6 \mathrm{to} 55 \mathrm{~Hz})$ <br> Vibration  <br> Dimensions  <br> Material  <br> (see "Technical Information") $\mathrm{D}-\mathrm{Housing}$ <br> Weight 200 g <br> Approvals UL |  |

## Wiring Diagram



S : signal wire.

## Accessories

| Socket $\diamond$ | D 411 |
| :--- | :--- |
| Socket cover | BB 5 |
| Hold down spring | HF |
| Front mounting bezel | FRS 2 |
| DIN-rail for D 411 | FMD 411 |
| For further information refer to "Accessories". |  |

## Operation Diagram



